Docket No.: GR 00 P 1679

JUL 0 2 2001

#### COMBINED DECLARATION AND POWER OF ATTORNEY IN ORIGINAL APPLICATION

As a below named inventor, I hereby declare that: my residence, post office address encleit Enship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

## TEST CONFIGURATION FOR THE FUNCTIONAL TESTING OF A SEMICONDUCTOR CHIP

described and claimed in the specification bearing that title, that I understand the content of the specification, that I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve month prior to this application, that I acknowledge my duty to disclose information of which I am aware which is material to the examination of this application under 37 C.F.R. 1.56a, and that no application for patent or inventor's certificate of this invention has been filed earlier than the following in any country foreign to the United States prior to this application by me or my legal representatives or assigns:

German Application No. 100 16 996.1, filed April 5, 2000, the International Priority of which is claimed under 35 U.S.C. §119.

I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

> HERBERT L. LERNER (Reg.No.20,435) LAURENCE A. GREENBERG (Reg.No.29,308) WERNER H. STEMER (Reg.No.34,956) RALPH E. LOCHER (Reg.No. 41,947)

Address all correspondence and telephone calls to:

LERNER AND GREENBERG, P.A. POST OFFICE BOX 2480 HOLLYWOOD, FLORIDA 33022-2480 Tel: (954) 925-1100

Fax: (954) 925-1101

I hereby state that I have reviewed and understand the contents of the aboveidentified specification, including the claims, as amended by any amendment referred to above.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

FULL NAME OF FIRST JO	DINT INVENTOR:	DIETER KANTZ
INVENTOR'S SIGNATURE  DATE: Hoey 17,  HENNT  Residence: MÜNCHEN,	2001 GSDORF	
Country of Citizenship:	GERMANY	h a
Post Office Address:	HEIGLHOFSTR. 45 L. D-81377 MÜNCHEN, AC GERMANY	INDENRING 15 6761 HENNIGSDORI
FULL NAME OF SECONE	) JOINT INVENTOR:	JOCHEN MÜLLER
INVENTOR'S SIGNATUR	E:	
DATE:		
Residence: MÜNCHEN,	GERMANY	
Country of Citizenship:	GERMANY	
Post Office Address:	HANSJAKOBSTR. 105, D-81825 MÜNCHEN, GERMANY	

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FULL NAME OF FIRST JOINT INVENTOR:	
<b>=</b> :	
GERMANY	
GERMANY	
HEIGLHOFSTR. 45 D-81377 MÜNCHEN, GERMANY	
) JOINT INVENTOR:	JOCHEN MÜLLER
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GERMANY	
GERMANY	
HANSJAKOBSTR. 105, D-81825 MÜNCHEN, GERMANY	
	GERMANY  GERMANY  HEIGLHOFSTR. 45 D-81377 MÜNCHEN, GERMANY  JOINT INVENTOR:  E: X John Miller  GERMANY  GERMANY  HANSJAKOBSTR. 105, D-81825 MÜNCHEN,

Docket No.:

GR 00 P 1679



5/ODEOC. Pourer E. Hillis 8-20-01

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicant** 

Dieter Kantz et al.

Appl. No.

09/826,594

Filed

April 5, 2001

Title

Test Configuration for the Functional Testing of a

Semiconductor Chip

Art Unit

2858

# **ASSOCIATE POWER OF ATTORNEY**

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231

#### Sir:

Please recognize GREGORY L. MAYBACK (Reg. No. 40,719) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

Lerner and Greenberg, P.A. P.O. Box 2480 Hollywood, Florida 33022-2480

> LAURENCE A. GREENBERG REG. NO. 29,308

Respectfully submitted,

Date: June 28, 2001

For Applicant(s)

Lerner and Greenberg, P.A.

Post Office Box 2480

Hollywood, FL 33022-2480

Tel:

(954) 925-1100

Fax:

(954) 925-1101

/mjb